

| L Number | Hits | Search Text | DB | Time stamp |
|----------|------|---|---|------------------|
| 2 | 23 | Douglass-s\$.in. and (asic or fpga or configurable or logic) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT | 2003/07/15 06:55 |
| 3 | 319 | ((716/17).CCLS.) not (Douglass-s\$.in. and (asic or fpga or configurable or logic)) | USPAT | 2003/07/15 07:18 |
| 4 | 358 | fpga same asic and (i adj2 o) | USPAT | 2003/07/15 08:01 |
| 5 | 41 | (fpga same asic and (i adj2 o)) and 716/\$.ccls. | USPAT | 2003/07/15 07:55 |
| 6 | 192 | (pld or programmable adj2 logic or fpga or pla) same (asic with embed\$4) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 07:56 |
| 7 | 14 | ((pld or programmable adj2 logic or fpga or pla) same (asic with embed\$4)) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 07:56 |
| 8 | 63 | fpga and (asic with core) | USPAT | 2003/07/15 08:02 |
| 9 | 14 | (fpga and (asic with core)) and 716/\$.ccls. | USPAT | 2003/07/15 08:02 |
| 10 | 11 | ((fpga and (asic with core)) and 716/\$.ccls.) not (((pld or programmable adj2 logic or fpga or pla) same (asic with embed\$4)) and 716/\$.ccls.) | USPAT | 2003/07/15 08:24 |
| 11 | 274 | fpga and (core same interface) | USPAT | 2003/07/15 09:18 |
| 12 | 650 | fpga and (core same interface) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 09:39 |
| 13 | 54 | (fpga and (core same interface)) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 09:18 |
| 14 | 46 | ((fpga and (core same interface)) and 716/\$.ccls.) not (fpga and (asic with core)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 09:18 |
| 15 | 717 | fpga and (asic same interface) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 09:39 |
| 16 | 9 | (fpga and (asic same interface)) and 716/17.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 09:39 |
| 17 | 37 | (fpga and (asic same interface)) and 716/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 09:39 |
| 18 | 28 | ((fpga and (asic same interface)) and 716/\$.ccls.) not ((fpga and (asic same interface)) and 716/17.ccls.) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2003/07/15 09:42 |
| 19 | 4 | ((("6353921") or ("5339262") or ("6178541") or ("6467073")).PN. | USPAT | 2003/07/15 09:50 |
| 20 | 0 | ("20030062922").PN. | USPAT | 2003/07/15 10:03 |
| 21 | 1 | ("20030062922").PN. | US-PGPUB | 2003/07/15 10:56 |

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|----|-------|---|---|------------------|
| 22 | 1058 | fpga same asic | USPAT | 2003/07/15 11:23 |
| 23 | 69 | ((fpga same asic) and 716/\$.ccls. | USPAT | 2003/07/15 10:56 |
| 24 | 69 | ((fpga same asic) and 716/\$.ccls.) and design\$3 | USPAT | 2003/07/15 10:56 |
| 25 | 1129 | configur\$4 same asic | USPAT | 2003/07/15 10:58 |
| 26 | 49 | ((configur\$4 same asic) and 716/\$.ccls. | USPAT | 2003/07/15 10:58 |
| 1 | 319 | (716/17).CCLS. | USPAT | 2003/07/15 13:09 |
| 27 | 113 | (asic or fixed) with core with interface | USPAT | 2003/07/15 11:31 |
| 28 | 5 | ((asic or fixed) with core with interface) and 716/\$.ccls. | USPAT | 2003/07/15 11:31 |
| 29 | 29 | fpga same (asic with core) | USPAT | 2003/07/15 11:33 |
| 30 | 21 | fpga and (asic with core) and 716/\$.ccls. | USPAT | 2003/07/15 11:34 |
| 31 | 16 | ((fpga and (asic with core) and 716/\$.ccls.) not (fpga same (asic with core)) | USPAT | 2003/07/15 11:34 |
| 32 | 17301 | fixed with core | USPAT | 2003/07/15 11:34 |
| 33 | 49 | ((fixed with core) same (configurable or fpga or reconfigurable or programmable) | USPAT | 2003/07/15 11:35 |
| 34 | 13835 | design same ((system near2 chip) or soc or fpga or plc or pld or programmable or configurable or asic) | USPAT | 2003/07/15 13:12 |
| 35 | 1102 | ((design same ((system near2 chip) or soc or fpga or plc or pld or programmable or configurable or asic)) and 716/\$.ccls. | USPAT | 2003/07/15 13:11 |
| 36 | 40094 | design same ((system near2 chip) or soc or fpga or plc or pld or programmable or configurable or asic or core or embed\$3) | USPAT | 2003/07/15 13:12 |
| 37 | 1283 | ((design same ((system near2 chip) or soc or fpga or plc or pld or programmable or configurable or asic or core or embed\$3)) and 716/\$.ccls. | USPAT | 2003/07/15 13:12 |
| 38 | 1038 | ((((design same ((system near2 chip) or soc or fpga or plc or pld or programmable or configurable or asic or core or embed\$3)) and 716/\$.ccls.) and (interface or interconnect or surround\$3) | USPAT | 2003/07/15 13:13 |
| 39 | 509 | (((((design same ((system near2 chip) or soc or fpga or plc or pld or programmable or configurable or asic or core or embed\$3)) and 716/\$.ccls.) and (interface or interconnect or surround\$3)) and (i near2 o) | USPAT | 2003/07/15 13:15 |
| 40 | 344 | ((((((design same ((system near2 chip) or soc or fpga or plc or pld or programmable or configurable or asic or core or embed\$3)) and 716/\$.ccls.) and (interface or interconnect or surround\$3)) and (i near2 o)) and (716/1 or 716/17 or 716/2 or 716/16) | USPAT | 2003/07/15 14:21 |
| - | 0 | ("US-553760-\$.DID. OR US-5652904-\$.DID. OR US-5671355-\$.DID. OR US-5752035-\$.DID. OR US-5970254-\$.DID. OR US-6020755-\$.DID. OR US-0609609-\$.DID. OR US-6279045-\$.DID. OR US-6282627-\$.DID. OR US-6343207-\$.DID.").PN. | USPAT | 2003/07/14 14:33 |
| - | 10 | US-0553760-\$.DID. OR US-5652904-\$.DID. OR US-5671355-\$.DID. OR US-5752035-\$.DID. OR US-5970254-\$.DID. OR US-6020755-\$.DID. OR US-0609609-\$.DID. OR US-6279045-\$.DID. OR US-6282627-\$.DID. OR US-6343207-\$.DID. | USPAT | 2003/07/14 14:33 |
| - | 39 | Douglass-s\$.in. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/07/14 14:36 |
| - | 23 | Douglass-s\$.in. and (asic or fpga or configurable or logic) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/07/15 06:54 |